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10/085892

2813  
10/25

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10086892	FILING DATE 03/04/2002	CLASS 438	SUBCLASS 1 +	GAU 2814	EXAMINER 2813 HOGAN
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\*\*APPLICANTS: Lee Kyo-Yeol;

\*\*CONTINUING DATA VERIFIED: *DA*  
None

\*\* FOREIGN APPLICATIONS VERIFIED: *DA*  
REPUBLIC OF KOREA 2001-11153 03/05/2001

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☒ yes ☐ no  
35 USC 119 conditions met ☒ yes ☐ no  
Verified and Acknowledged Examiners's initials *DA*

ATTORNEY DOCKET NO  
030681-366

TITLE : Method for fabricating group III-V compound semiconductor substrate

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED

CLAIMS ALLOWED

Total Claims  
Print Claim for  
O.G.

Assistant Examiner

ISSUE FEE

Amount Due  
Date Paid

DRAWING

Sheets Drawg.  
Figs. Drawg.  
Print Fig.

Primary Examiner

☐ TERMINAL  
DISCLAIMER

PREPARED FOR ISSUE

Application Examiner

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